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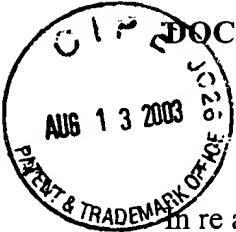
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sharad Kapur, et al.

Serial No.: 09/427,238

Filed: October 26, 1999

For: SYSTEM AND METHOD FOR DETERMINING CAPACITANCE  
FOR LARGE-SCALE INTEGRATED CIRCUITS

Group No.: 2123

Examiner: Eduardo Garcia-Otero

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on 08/14/2003 (Date)  
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AMENDMENT UNDER 37 C.F.R. § 1.111

The Applicants have carefully considered this application in connection with the Examiner's Action mailed on May 21, 2003, and respectfully request reconsideration of this application in view of the following amendment and remarks.